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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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Four State Asynchronous Architectures - group of 6 »

AJ McAuley - IEEE Transactions on Computers, 1992 - doi.ieeecomputersociety.org

... one cell. The design of an **asynchronous** 2 : 1 **multiplexer** concludes this

section. Sec- 0018-9340/92\$03.00 0 1992 IEEE Page 2. 130 ...

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... **Multiplexer** 4 Keys ... In order to achieve this, **asynchronous** circuits must be

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... For example, whilst changing the delay selection **multiplexer** when a transition is ...

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... 3 Implementation of the Cop component This Section presents an **asynchronous**

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... logic design, not covered here, where **asynchronous arbitration** takes place ... where

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... KUMAR et al.: ANALOG VLSI CHIP WITH **ASYNCHRONOUS** INTERFACE ... address is applied to

the **multiplexer**, which steers ... The **arbitration** logic and the encoder generate a ...

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... 1 Fig. 1. Hardware block diagram of link **multiplexer**. ... **asynchronous** stream has the

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... designs differ in the **arbitration** mechanisms and ... often be accomplished by a **multiplexer**, which must be ... not specifically targeted to **asynchronous** circuits will ...

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... Therefore, perfect channel **arbitration** can be achieved ... used for the dual-channel **multiplexer** are described ... delay-throughput performance of **asynchronous** data are ...

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